## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of
Invention

LOW CAPACITANCE FET FOR OPERATION AT SUBTHRESHOLD VOLTAGES

Application Number:

Date:

First Named Applicant:

Brent A. Anderson

Confirmation Number:

Attorney Docket Number:

BUR920040100US1

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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files	
us-assignment	BUR920040100US1-usassn.xml	
-	us-assignment.xsl us-assignment.dtd	
	BUR920040100Assign1.tif	
	BUR920040100Assign2.tif	
us-request	BUR920040100US1-usrequ.xml	
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us-fee-sheet	BUR920040100US1-usfees.xml	
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us-ids	BUR920040100US1-usidst.xml	
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us-declaration	BUR920040100Dec1.tif	
us-declaration	BUR920040100Dec2.tif	
application-body	BUR920040100-trans.xml	
	us-application-body.xsl	
	application-body.dtd	
	wipo.ent	
	mathml2.dtd	
	mathml2-gname-1.mod	
	isoamsa.ent	
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	isoamsn.ent	
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	BUR920040100drw3.tif	
	BUR920040100drw4.tif	
	BUR920040100drw5.tif	

BUR920040100drw6.tif	
Comments	
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